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#### Application Information

Title Line One:: SEMICONDUCTOR DEVICE AND METHOD OF  
Title Line Two:: MANUFACTURE THEREOF, CIRCUIT BOARD  
Title Line Three:: AND ELECTRONIC INSTRUMENT  
Title Line Four::  
Total Drawing Sheets:: 4  
Docket Number:: 108102

#### Continuity Information

>This application is a:: U.S. National Stage of  
Application One:: PCT/JP00/03051  
Filing Date:: May 12, 2000

#### Prior Foreign Applications

Foreign Application One:: 11-133426  
Filing Date:: May 14, 1999  
Country:: JAPAN  
Priority Claimed:: YES